

**Amendments to the Specification:**

Pg. 4, lines 4-9: *Please replace the paragraph with the following amended paragraph:*

The ~~case~~ housing 12 includes a second window 18 for viewing a display 20. The display 20 is preferably a liquid crystal display (LCD) that selectively displays such information as the date, the time, tone arm position, tone arm movement, elapsed time during the auditing session, chosen display language and other pertinent information. Three buttons 22A-C are associated with the display 20 to select and/or change the displayed language, time, date, etc. One of the buttons is used to select a desired menu from a series of sequentially displayed menu titles. The remaining two buttons are respectively utilized to move a selection bar with respect to the menu to choose from among a plurality of listed options. The first button then functions as a select button to select the option so identified

Pg. 5, lines 6-27: *Please replace the paragraph with the following amended paragraph:*

A rotary knob 24 is utilized to select the appropriate range for the meter 16 as described below, and is typically referred to as the "TA" knob by experienced users of these devices. The term "TA" will accordingly be used herein to refer to meter range setting from time to time. The TA knob 24 is preferably coupled to an optical encoder within the housing that produces a digital value indicative of the knob's rotational position. The rotational position of the knob may conveniently be thought of in terms of the number of degrees it has been rotated from its counterclockwise endpoint, but is conveniently discussed in terms of the TA value represented by its position. The knob is accordingly shaped at 24A to point to a TA value on a stationary, circumferentially disposed numeric scale 36 imprinted on the case 12. The scale is preferably provided with gradations marked from "0" to "6" over an arc of approximately 240°, and the knob is typically rotated clockwise from a TA value of 0.5 to a TA value of 6.5 during the auditing procedure. It should be noted that the numbers and spacing have been chosen to be consistent with prior devices such as those illustrated and described in U.S. 4,459,995 and that any series of numbers, letters or other markings arranged about any convenient arc could be utilized without departing from the spirit of the invention.

Pg. 6, lines 3-15: Please replace the paragraph with the following amended paragraph

Figure 2 is a top view of the preferred meter 16. The meter is preferably a moving-coil meter capable of reading from 0 to 100 microamps at full scale deflection, and has an arcuate scale 38 divided into sections. At approximately one-third of the distance from its left endpoint, the scale displays a small sector of arc marked "SET". In operation, and during the pre-operation calibration of the meter, its needle 17 is described as being "at SET" when the needle points to the segment of the scale labeled as "SET". The TA knob 24 is used to periodically bring the needle back to the area near SET during the auditing process, and the sensitivity of the meter is adjusted using the buttons ~~22a-22e~~ 28a-28c and knob 26 before or during the auditing procedure to obtain appropriately meaningful needle deflections. Preferably, no more than 50 micro-amps of electrode current flows through the audited person's body. This level has been found to assure the person's comfort, while providing properly responsive needle "reads" as the person is monitored during auditing.

Pg. 6, line 29 – pg. 7, line 7, line 2: Please replace the paragraph with the following amended paragraph

The front-end circuitry 100 comprises a resistance sensing circuit 108 for producing a measurement signal indicative of body resistance, a voltage follower 110 for filtering and isolating the measuring signal from the effects of other system components, and an analog-digital converter ~~120a~~ 120 for converting the measurement signal to a digital value indicative of body resistance measured by the resistance sensing circuit.

Pg. 7, lines 3-12: Please replace the paragraph with the following amended paragraph:

The resistance sensing circuit is preferably of the voltage divider type electrically coupled between a positive D.C. source voltage ~~V<sub>DD</sub>~~ AV<sub>DD</sub> and ground ~~GND~~ AGND. During the auditing session, the resistance sensing circuit comprises a 4.99K resistor R1, the body resistance R<sub>pc</sub> (as sensed between the electrodes 30, 32 coupled to pins 1 and 3

1 of jack 104), and a 45.3K resistor R3, all coupled in series between the D.C. source ~~V<sub>DD</sub>~~  
2 AV<sub>DD</sub> and ground. When the monitoring electrodes have been disconnected from the  
3 jack 104, the jack is configured to electrically couple pins 2 and 3 together, placing the ~~5k~~  
4 4.99k resistor R2 across the jack.

5  
6 Pg. 9, lines 21-33: *Please replace the paragraph with the following amended*  
7 *paragraph:*

8 Accordingly, the CPU 400 ~~sends~~ sends appropriate selection signals to pins 9, 10,  
9 11 of the ~~multiplier~~ multiplexor 102 upon power-up to cause its terminal X to be  
10 sequentially coupled to terminals X1, X2, and X3, respectively placing a 4.99K resistor  
11 R2, a 12.4K resistor R4, and a short circuit in series with resistors R1 and R3 in lieu of  
12 R<sub>pc</sub>. The value of eo under each condition is fed to the operational amplifier 110,  
13 digitized by the converter 120 and outputted to the central processing unit 400. The above  
14 values were chosen for R2 and R4 because the TA ranges of “2” and “3” have historically  
15 been the most commonly used settings when monitoring body resistance. The short  
16 circuit condition is used to easily provide an additional data points. An open-circuit  
17 condition could be used as well.

18  
19 Pg. 9, lines 21-33: *Please replace the paragraph with the following amended*  
20 *paragraph:*

21 Accordingly, the central processor 400 provides an automatically correcting gain  
22 factor to the meter drive signal for the purpose of substantially ~~increasing the~~ eliminating  
23 the possibility of masked and false readings across the usable range of TA values. The  
24 preferred gain factors are:

25  
26 Pg. 9, lines 21-33: *Please replace the paragraph with the following amended*  
27 *paragraph:*

28 Likewise, the medium sensitivity range button ~~26b~~ 28c and low sensitivity range  
29 button ~~26e~~ 28b, are respectively assigned column addresses of 2 and 3, and the  
30 conductors associated with these columns are respectively monitored by pins 52 and 53

1 of the CPU. Those skilled in the art will recognize that the use of digital amplification  
2 eliminates the unwanted amplification of noise the would accompany the amplification of  
3 an analog signal value, rendering small changes in body resistance more visually  
4 perceptible with the subject device.

5  
6 Pg. 15, line 26 – pg. 16, line 2: *Please replace the paragraph with the following*  
7 *amended paragraph:*

8         The sensitivity knob position signal 532 is applied to pin 93 of the CPU 400,  
9 where it is internally coupled to an analog-digital converter that produces a digital value  
10 indicative of the knob's setting. As illustrated in Figure 6, the sensitivity knob is  
11 mechanically coupled to the wiper ~~526A~~ 524A of a potentiometer ~~526~~ 524 that is serially  
12 coupled between a 10K resistor ~~530~~ 525 and a 10K resistor ~~532~~ 526 in circuit between  
13 the DC source voltage VDD and ground GND. The resistor ~~530~~ 525, potentiometer ~~526~~  
14 524 and resistor ~~532~~ 526 form a voltage divider network. The sensitivity knob position  
15 signal 532 is accordingly a DC level signal that increases as the knob is turned clockwise  
16 and the wiper moves away from ground.

17  
18 Pg. 16, line 27- pg. 17, line 2: *Please replace the paragraph with the following*  
19 *amended paragraph*

20         The ~~current-drive~~ control circuit 606 comprises an operational amplifier 608,  
21 which receives the analog output signal 604 at its non-inverting input. The output of the  
22 operational amplifier 608 is partially fed back to its inverting input to a degree controlled  
23 by a digital potentiometer ~~610~~ 616 in the feedback loop whose resistance is set by data  
24 received at pins 1, 2 from the processor 400. The digital potentiometer 610 is adjusted  
25 during the assembly process to provide a desired amount of meter dampening, and the  
26 values applied by the CPU preserve that dampening characteristic.